

Meet the Editors

KENICHI MORITA



Kenichi Morita is a professor emeritus of Hiroshima University. He received his B. Eng., M. Eng., and Dr. Eng. degrees from Osaka University in 1971, 1973, and 1978, respectively. From 1974 to 1987, he was a research associate of the Faculty of Engineering Science, Osaka University. From 1987 to 1990, he was an associate professor, and from 1990 to 1993 a professor of the Faculty of Engineering, Yamagata University. From 1993 to 2013, he was a professor of the Graduate School of Engineering, Hiroshima University.

He has been engaged in the research of automata theory, reversible computing, and formal language theory. Related to unconventional computing, he studied many kinds of reversible computing models. They are universal

reversible Turing machines, reversible counter machines, reversible cellular automata, reversible logic circuits composed of 2-state reversible logic elements, and some others. In spite of the constraint of reversibility, even very simple systems of the above models were turned out to be computationally universal.

REFERENCES

- [1] K. Morita. (1996). Universality of a reversible two-counter machine. *Theoret. Comput. Sci.*, 168:303–320.
- [2] K. Morita. (2008). Reversible computing and cellular automata — A survey. *Theoret. Comput. Sci.*, 395:101–131.
- [3] K. Morita. (2012). *Reversible Computing* (in Japanese). Kindai Kagaku-sha Co., Ltd., Tokyo, ISBN 978-4-7649-0422-4.
- [4] K. Morita. (2015). Universal reversible Turing machines with a small number of tape symbols. *Fundam. Inform.*, 138:17–29.
- [5] Y. Mukai, T. Ogiro, and K. Morita. (2014). Universality problems on reversible logic elements with 1-bit memory. *Int. J. Unconventional Computing*, 10:353–373.